



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Farnworth et al.

**Serial No.:** 10/801,254

**Filed:** March 16, 2004

**For:** DEVICE AND METHOD FOR  
TESTING INTEGRATED CIRCUIT DICE  
IN AN INTEGRATED CIRCUIT MODULE

**Examiner:** Unknown

**Group Art Unit:** 2811

**Attorney Docket No.:** 2269-2883.4US  
(96-0206.04/US)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

June 9, 2004  
Date

Signature

Deidra Pfeil  
Name (Type/Print)

**REQUEST FOR CORRECTED FILING RECEIPT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the official filing receipt received from the PTO in the above application. Issuance of a corrected filing receipt in accordance with the annotations on the attached filing receipt copy is respectfully requested.

The errors to be corrected are as follows:

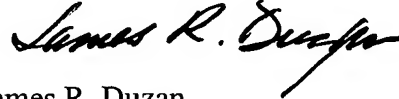
**IN THE APPLICANT(S):**

Please change "Waden" to --Warren--.

Serial No.: 10/801,254

These corrections are not due to any errors by Applicants and no fee is due.

Respectfully submitted,



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Date: June 9, 2004  
JRD/sls:djp

Enclosure: Copy of Official Filing Receipt with annotations correcting errors  
Document in ProLaw

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/801,254	03/16/2004	2811	856	2883.4US (96-0206.04/US)	14	12	4

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TRASKBRITT, P.C.

CONFIRMATION NO. 2282

FILING RECEIPT



\*OC000000012833633\*

Date Mailed: 06/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) Warren

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James M. Wark, Boise, ID;  
Eric S. Nelson, Boise, ID;  
Kevin G. Duesman, Boise, ID;

**Domestic Priority data as claimed by applicant**

This application is a CON of 10/396,163 03/25/2003  
which is a CON of 09/797,368 03/01/2001 PAT 6,605,956  
which is a CON of 09/097,427 06/15/1998 PAT 6,240,535  
which is a CIP of 08/718,173 09/19/1996 PAT 5,796,746  
which is a CIP of 08/577,840 12/22/1995 PAT 5,825,697  
and is a CIP of 08/666,247 06/20/1996 PAT 5,764,574

**Foreign Applications**

If Required, Foreign Filing License Granted: 06/01/2004

Projected Publication Date: 09/09/2004

Non-Publication Request: No

Early Publication Request: No

Title

Device and method for testing integrated circuit dice in an integrated circuit module

Preliminary Class

257

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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